ABSTRACT

MICROPROCESSOR SYSTEM

A microprocessor-based system includes multiple peripherals, which can be

5   accessed by the microprocessor over a system bus, with the aid of address
decoding logic. Depending on the required functionality of the system at any time,
one or more of the peripherals can be disabled. When a peripheral device is
disabled, the address decoding logic of the system is modified to ensure that no
attempts are made to access that peripheral device.

10